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\*Admitted only in Maryland  
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•Practice Limited to  
Federal Agencies

July 22, 2005

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Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

**Art Unit 2816**

Re: U.S. Utility Patent  
Patent No. 6,906,564; Issued: June 14, 2005  
For: **Apparatus and Method for Delay Matching of Full and Divided Clock  
Signals**

Inventor: Kwang Y. Kim  
Our Ref: 1875.1710002

**Certificate**

**JUL 28 2005**

**of Correction**

Sir:

Transmitted herewith for appropriate action are the following documents:

1. Request for Certificate of Correction Under 37 C.F.R. §1.323 for Applicant's Mistake;
2. Completed Form PTO/SB/44, with the noted corrections printed thereon; and
3. Return postcard.

It is respectfully requested that the attached postcard be stamped with the date of filing of these documents, and that it be returned to our courier. In the event that extensions of time are necessary to prevent abandonment of this patent application, then such extensions of time are hereby petitioned.


**JUL 29 2005**

Commissioner for Patents  
July 22, 2005  
Page 2

The U.S. Patent and Trademark Office is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

STERNE, KESSLER, GOLDSTEIN & FOX P.L.L.C.

*for*   
Jason D. Eisenberg  
Attorney for Patentee  
Registration No. 43,447  
Reg. No. 33,876

JDE/slp  
Enclosures

423559\_1.DOC

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE



In re patent of:

Kwang Y. Kim

Patent. No.: 6,906,564

Issued: June 14, 2005

For: **Apparatus and Method for Delay  
Matching of Full and Divided Clock  
Signals**

Confirmation No.: 4388

Art Unit: 2816

Examiner: M. Ton

Atty. Docket: 1875.1710002

**Request for Certificate of Correction  
Under 37 C.F.R. § 1.323 For Applicant's Mistake**

*Attn: Certificate of Correction Branch*

Commissioner for Patents  
PO Box 1450  
Alexandria, VA 22313-1450

Sir:

It is hereby requested that a Certificate of Correction under 37 C.F.R. § 1.323 be issued for the above-captioned United States Patent. This Certificate of Correction is being requested due to mistakes which appear in the printed patent. The mistakes made by Applicant are of a clerical or typographical nature, or of a minor character. Patentee submits that correction of these errors does not introduce new matter.

Specifically, the printed patent contains the following errors for which a Certificate of Correction is respectfully requested:

In column 10, line 41, "coupling a first resistor between the a first output terminal" should appear as --coupling a first resistor between the first output terminal--.

In column 10, line 49, "coupling a second clock buffer devices" should appear as --coupling a second clock buffer device--.

07/26/2005 WABDELRI 00000009 190036 6906564  
01 FC:1811 100.00 DA

**Remarks**

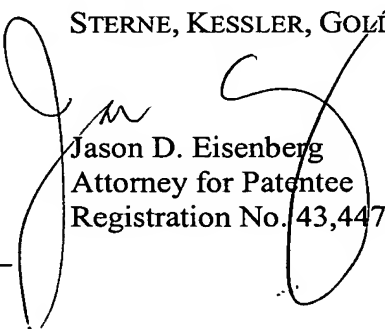
The above-noted corrections do not involve such changes in the patent as would constitute new matter or would require reexamination.

A completed Form PTO/SB/44 accompanies this request, with the above-noted corrections printed thereon. Accordingly, a Certificate of Correction is believed proper and issuance thereof is respectfully requested.

This request is accompanied by payment of the fee set forth in 37 C.F.R. § 1.20(a). Fee payment is provided in the attached Credit Card Payment Form (PTO-2038). The Commissioner is hereby authorized to charge any fee deficiency, or credit any overpayment, to our Deposit Account No. 19-0036.

Respectfully submitted,

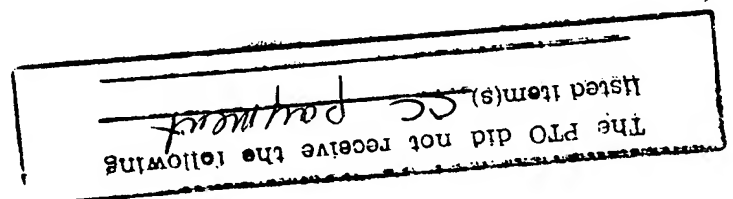
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Attorney for Patentee  
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Date: July 22, 2005

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JUL 29 2005

**UNITED STATES PATENT AND TRADEMARK OFFICE  
CERTIFICATE OF CORRECTION**Page 1 of 1

PATENT NO. : 6,906,564  
APPLICATION NO.: 10/622,708  
ISSUE DATE : June 14, 2005  
INVENTOR(S) : Kwang Y. Kim

It is certified that an error appears or errors appear in the above-identified patent and that said Letters Patent is hereby corrected as shown below:

In column 10, line 41, "coupling a first resistor between the a first output terminal" should appear as --coupling a first resistor between the first output terminal--.

In column 10, line 49, "coupling a second clock buffer devices" should appear as --coupling a second clock buffer device--.

**MAILING ADDRESS OF SENDER (Please do not use customer number below):**

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This collection of information is required by 37 CFR 1.322, 1.323, and 1.324. The information is required to obtain or retain a benefit by the public which is to file (and by the USPTO to process) an application. Confidentiality is governed by 35 U.S.C. 122 and 37 CFR 1.14. This collection is estimated to take 1.0 hour to complete, including gathering, preparing, and submitting the completed application form to the USPTO. Time will vary depending upon the individual case. Any comments on the amount of time you require to complete this form and/or suggestions for reducing this burden, should be sent to the Chief Information Officer, U.S. Patent and Trademark Office, U.S. Department of Commerce, P.O. Box 1450, Alexandria, VA 22313-1450. DO NOT SEND FEES OR COMPLETED FORMS TO THIS ADDRESS. SEND TO: **Attention Certificate of Corrections Branch, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.**

*If you need assistance in completing the form, call 1-800-PTO-9199 and select option 2.*